

AD-D96 User Guide

revised 121808

Protecting Against Electrostatic Discharge

Static electricity can harm delicate components on the AD-D96 board. To prevent static damage, discharge static electricity from your body before you touch any electronic components. You can do so by touching an unpainted metal surface on the HV-3D's chassis.

Unplug the HV-3D's AC power cord before removing the top cover and changing settings on the AD-D96 board.

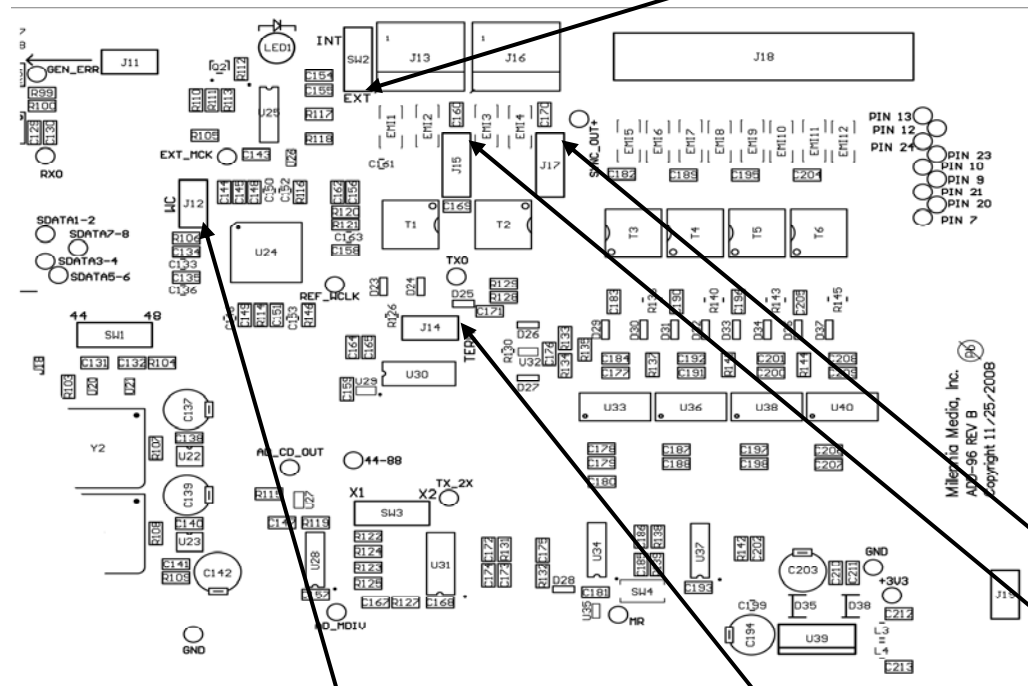
Clocks - the AD-D96 should have only one clock source. If running with the internal clock, make sure SW2 is in the INT position and no external clock is connected.

Sync Signals on the BNC connectors should conform to the AES-3id specification. AES-3, through an XLR-to-BNC transformer (Neutrik NADITBNC-F or equivalent) will work fine. S/PDIF works, but is not specified.

Sample Rate kHz	Rear Panel	
44.1*	X1	44
48	X1	48
88.2	X2	44
96	X2	48

Clock Source	SW2
Internal	INT
External*	EXT

*The arrows point to the default jumper positions.



Clock Input	J12
Word Clock*	on
AES	off

Clock Termination	J14
110 ohms*	on
Unterminated	off

Clock Output	J15	J17
Word Clock*	2,3	2,3
AES	1,2	1,2